



## Test Procedure for the LV47009PGEVB Evaluation Board

### TEST Procedure

Prepare “DC POWER SUPPLY”, capability is 40V or more and 20A or more. And “Digital MULTIMETER”, and “4ohm speaker” and “Oscillator” and “4ch Oscilloscope” and “Heat sink”

#### 1. Recommendation “Heat sink”

Material: Aluminium alloy (A6000 type)

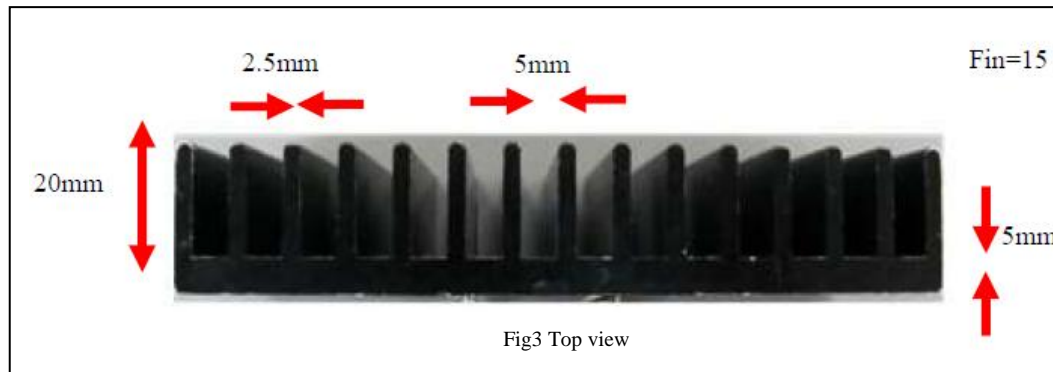
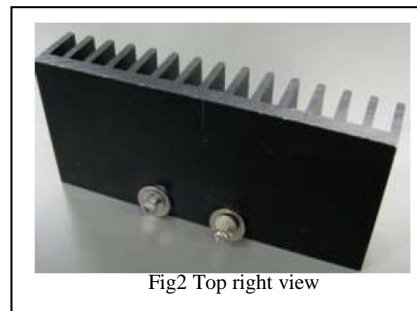
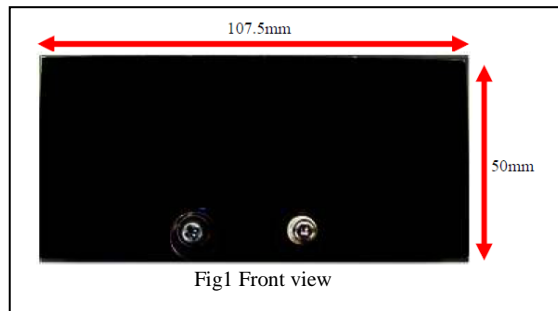
Surface treatment: Alumite (color : black)

Thermal resistance of heat sink ( $\theta_f$ ): 3 degree C/W

Thermal resistance between the junction and case: 1 degree C/W

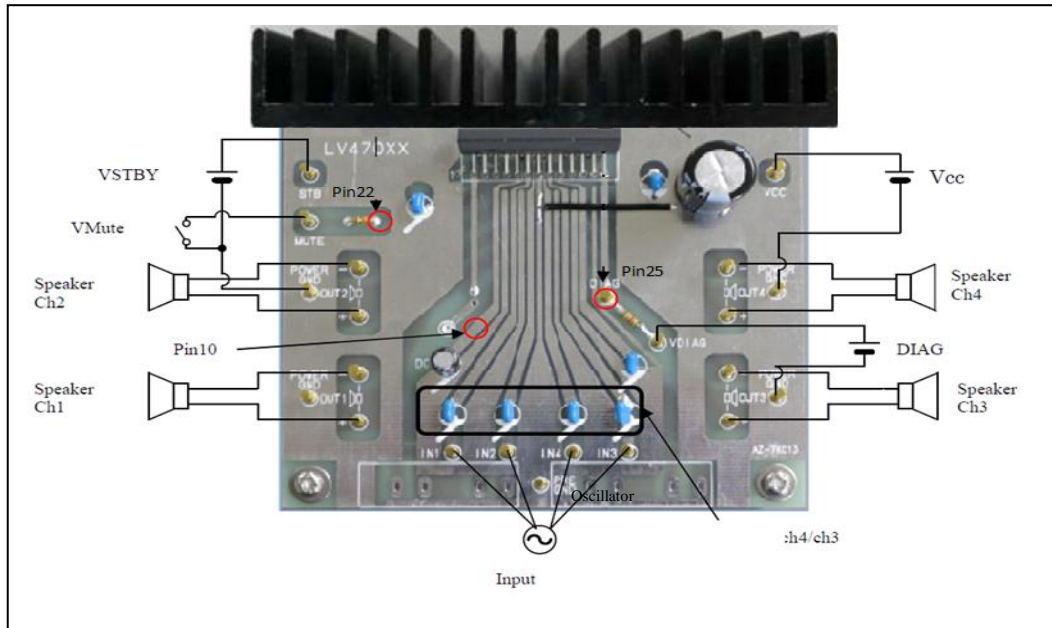
$\theta_{jc} + \theta_f = 4$  degree C/W

$\{ 150 \text{ degree C}(T_{jmax}) - 25 \text{ degree C}(T_a) \} \div 4 \text{ degree C/W} \rightarrow P_{dmax} \approx 30W$





**2. Eva-board and Measurement instruments of cable connection.**



**3. VCC, Input, Output operation sequence Start up**

- A. Vcc ON (Vcc=8V to 18V)
- B. AMP ON ( STBY Pin=High: VSTBY=2.5V to Vcc)
- C. Mute OFF (Mute Pin=open: VMute switch close →open)
- E. Input signal ON

**Shutdown**

- F. Input signal OFF
- G. Mute ON (Mute Pin=Low: Vmute switch open → close)
- H. AMP OFF (STBY Pin=Low: VSTBY=0V)
- I. Vcc OFF